

20/GO/TO

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

|  |   |   |                |                     |                      |
|--|---|---|----------------|---------------------|----------------------|
| APPL NUM<br>10033793   | FILING DATE<br>01/03/2002               | CLASS<br>257  | SUBCLASS<br>73 | GAU<br>2649<br>2815 | EXAMINER<br>MMEH/ORD |
| <b>**APPLICANTS:</b> Hayashi Norio; Hagino Noriyoshi; Matsui Toshiki; Watanabe Kazuo;<br>Tanaka Satoshi; |   |   |                |                     |                      |
| <b>**CONTINUING DATA VERIFIED:</b>   |   |   |                |                     |                      |
| <h1>BEST AVAILABLE COPY</h1>   |   |   |                |                     |                      |
| <b>** FOREIGN APPLICATIONS VERIFIED:</b><br>JAPAN 2001-005241 01/12/2001                                 |   |   |                |                     |                      |
| PG-PUB   | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/>  |                |                     |                      |
| Foreign priority claimed   |   | yes <input checked="" type="checkbox"/> no <input type="checkbox"/>   |                | ATTORNEY DOCKET NO  |                      |
| 35 USC 119 conditions met  |   | yes <input checked="" type="checkbox"/> no <input type="checkbox"/>   |                | XA-9605             |                      |
| Verified and Acknowledged Examiners's initials   |   | TITLE : Signal processing semiconductor integrated circuit device and wireless communication system<br>U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94) |                |                     |                      |

|  |                  |                           |                             |                   |                            |
|--|------------------|---------------------------|-----------------------------|-------------------|----------------------------|
| <b>NOTICE OF ALLOWANCE MAILED</b>  |                  | <b>Assistant Examiner</b> | <b>CLAIMS ALLOWED</b>       |                   |                            |
|  |                  |                           | <b>Total Claims</b>         |                   | <b>Print Claim for O.G</b> |
| <b>ISSUE FEE</b>   |                  | <b>Primary Examiner</b>   | <b>DRAWING</b>              |                   |                            |
| <b>Amount Due</b>  | <b>Date Paid</b> |                           | <b>Sheets Drwg.</b>         | <b>Figs.Drwg.</b> | <b>Print Fig.</b>          |
|  |                  |                           |                             |                   |                            |
| <input type="checkbox"/> <b>TERMINAL</b><br><br><b>DISCLAIMER</b>  |                  | <b>PREPARED FOR ISSUE</b> | <b>Application Examiner</b> |                   |                            |
| <b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. |                  |                           |                             |                   |                            |

**FILED WITH:**

☐ DISK (CRF)

 **CD-ROM**  
(on right inside flap)